



# EVOLUTION OF INTELLIGENT SYSTEMS



MAY 11, 2022 ORGANIZED BY IFERP

## Evolution of intelligent systems

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#### **1. EVENT SUMMARY**

Webinar Name: Evolution of intelligent systemsDate: 11th May 2022Mode/Place: Virtual (Zoom)No of Registrations: 847No of Participants: 335

#### **1.1 OVERVIEW**

A Free Faculty Development Programme On "Evolution of intelligent systems" Organized by Institute For Engineering Research And Publications (IFERP) on 11th May 2022 at 11 AM to 12 PM (IST) on Online platform. Prof. Priyadarsan Patra was the Guest speaker of this FDP. In total we received 847 Registration across the global and gained 4 views through Zoom and the purpose of the guest lecture is achieved and earned good comments from the Participants.

#### **1.2 ABOUT THE WEBINAR**

Intelligent systems are technologically advanced machines that perceive and respond to the world around them. Over the last two decades the field of Intelligent Systems delivered to humankind significant achievements, while also facing major transformations. 20 years ago, automation and knowledge-based AI were still the dominant paradigms fuelling the efforts of both researchers and practitioners. Later, 10 years ago, statistical machine intelligence was on the rise, heavily supported by digital computing, and led to the unprecedented advances in and dependence on digital technology.

#### 2. Welcoming and Opening Ceremony

Welcoming Keynote Speaker of the webinar on Evolution of intelligent systems.

**3.Keynote Speaker** 

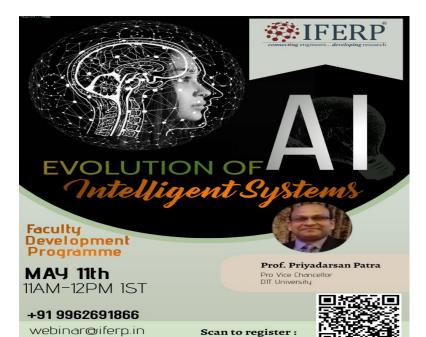


**Prof. Priyadarsan Patra** Pro Vice Chancellor | Humanist | Distinguished Professor | ex Chief Architect | IE Fellow | Founding Chair, IEEE SVDTC and SEEDS Dehradun, Uttarakhand, India

#### Biography:

Prof. Priyadarsan Patra is a distinguished Professor and Pro Vice-Chancellor at DIT University . He is also an accomplished scientist, technologist, and chief architect with a proven track record of 20+ years in creating and leading world-class research and development to support Server, tablet/phone SoC and Client processor architectures for power-performance optimized designs, and their Validation, Debug, and Test for several generations of flagship silicon devices/platforms that are shipped in millions. His area of Expertise includes Micro-architecture for Validation, Debug, and HVM Functional-Test; Low-power design and systems; R&D; Strategic Planning; Co-optimized software, firmware, hardware ingredients; CAD research and prototyping; Algorithm design; Applications of AI and Deep learning; Solving complex problems innovatively. He has also Led across-org virtual teams to success through collaboration and influence.

On behalf of the organizing committee, we released the Poster and HTML templates for the Webinar.



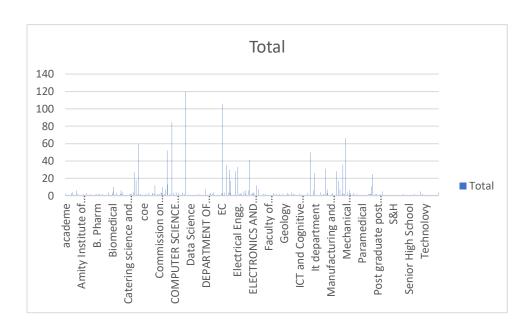
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**5. Registered Statistics** 

Country	No of Registrations
AE	5

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AF	1
AR	2
AZ	1
BD	2
BG	1
ВН	2
ВТ	1
CN	1
DE	1
DZ	3
EG	2
ET	1
FR	1
GB	2
GH	2
ID	10
IN	1465
IQ	4
IT	1
OL	3
JP	2
КЕ	1
KW	1
LK	5
LY	1
МА	1
MV	1
MY	4
MZ	1
NG	13
NP	6
OM	3
Other	10
PE	2
РН	144
РК	11
QA	1





#### 6. Conclusion:

A Free Faculty Development Programme On "Evolution of intelligent systems" Promotions was started before a week of the Guest Lecture and the response & reach of the guest lecture was well good & gained more people into the APR. Due to the lack of time, we can't be able to cover the foreign countries, but we get few responses from other countries as well. Overall, it's a one of the successful event in 2022 in a short duration. Received a positive response from the students and academicians about the Guest Lecture.